Docket No. 040076.01

Application No. 10/021,012

APPENDIX

Changes to Claims:

The following is a marked-up version of the amended claim(s):

- (Twice Amended) A liquid crystal panel substrate, comprising:

 reflecting electrodes formed on a substrate;

 a switching element formed corresponding to each of the reflecting electrodes;

 a passivation film formed on said reflecting electrodes comprising a silicon oxide film; and
- a silicon nitride film formed as an insulating interlayer between said reflecting electrodes and a metal shielding layer above the switching element thereunder having moisture resistance.
 - (Amended) A liquid crystal panel substrate comprising:
- a pixel region having a matrix of reflecting electrodes formed on a substrate and a switching element formed corresponding to each of said reflecting electrodes, a periphery region of said pixel region on the substrate having a metal layer and an at least insulating interlayer interlayers; and
- a passivation film having a laminate structure comprising a silicon oxide film and a silicon nitride film on said silicon oxide film, the passivation film being formed at <u>least</u> on side of edge sections of the <u>metal layer and the at least</u> insulating <u>interlayer interlayers</u>.
 - 4. (Amended) A liquid crystal panel substrate comprising:
- a pixel region having a matrix of reflecting electrodes formed on a substrate and a transistor formed corresponding to each of the reflecting electrodes;
- a peripheral circuit arranged in a periphery region of said pixel region on the substrate for supplying signals to said transistors in said pixel region;

Docket No. 040076.01

Application No. 10/021,012

a first passivation film comprising a silicon oxide film formed on said reflecting electrodes in said pixel region; and

a second passivation film comprising a silicon nitride film formed at least on a side of edge sections of said periphery region.

9. (Amended) A liquid crystal panel substrate according to claim 3, said edge section of said-metal layer and the insulating interlayer interlayers being a scribed region of the substrate.